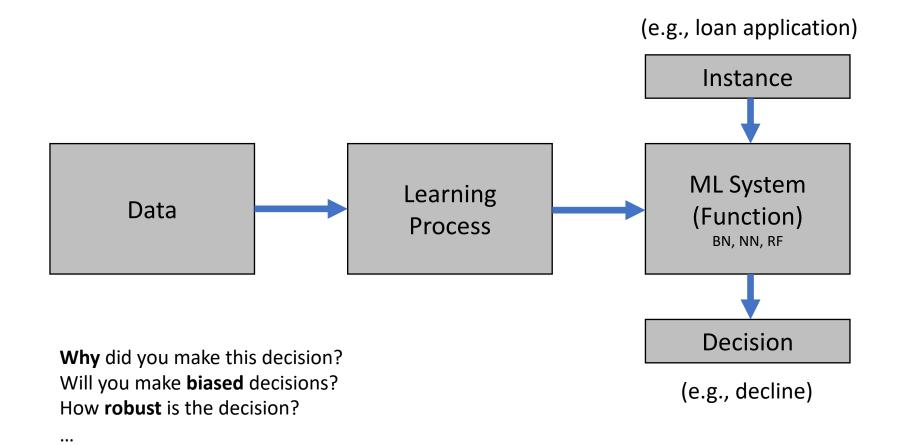
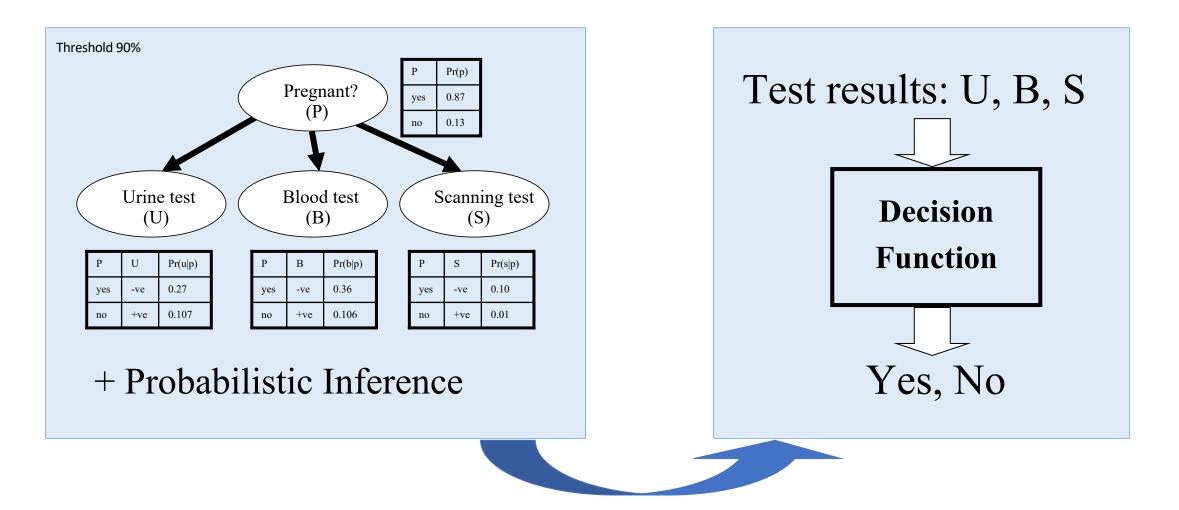
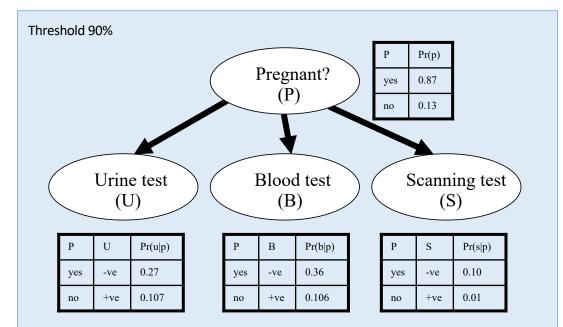
Reasoning About What Was Learned

Adnan Darwiche Computer Science Department UCLA

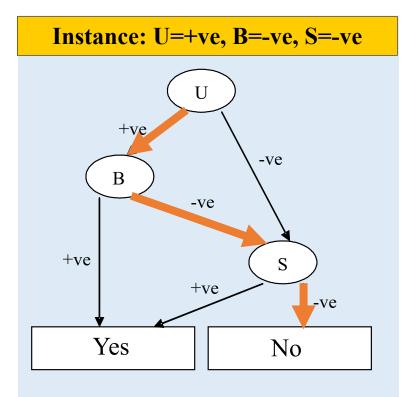
ICLA 2021



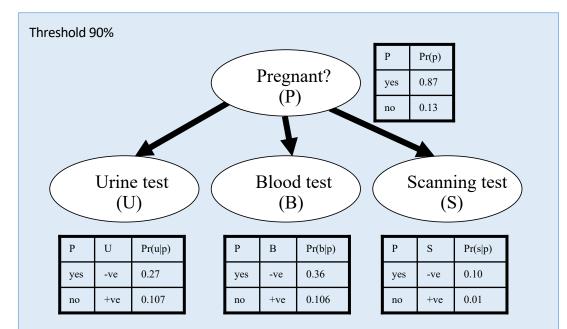




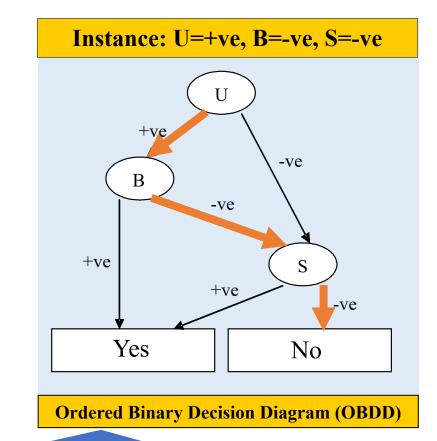
+ Probabilistic Inference

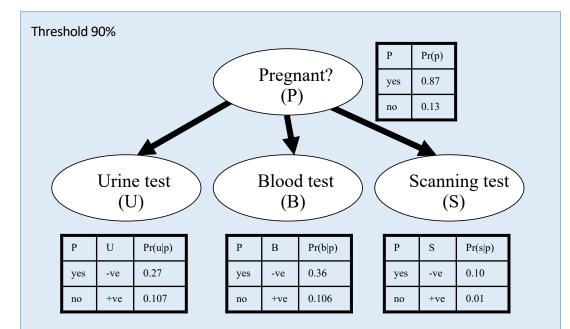






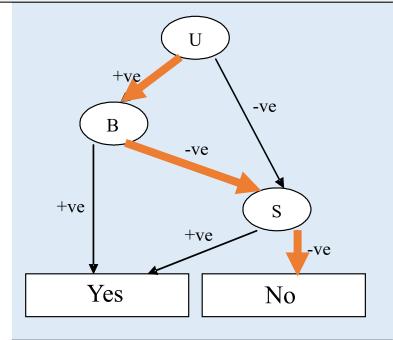
+ Probabilistic Inference





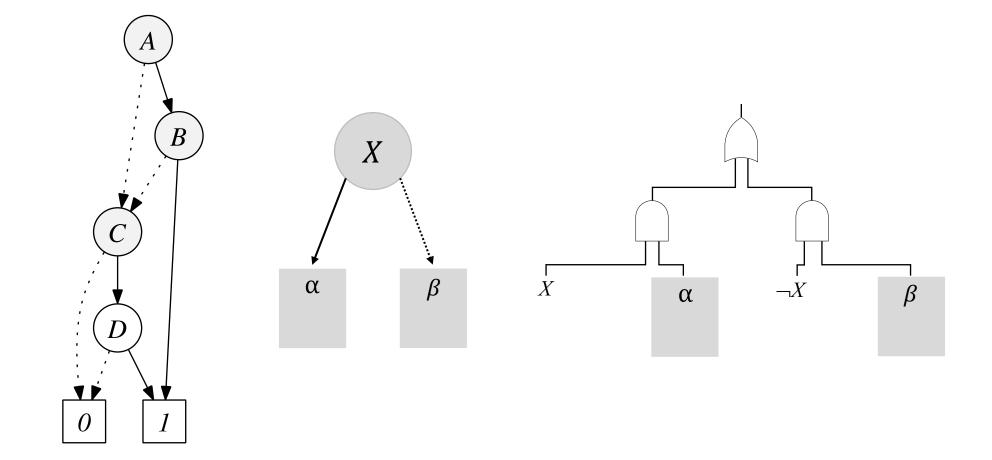
+ Probabilistic Inference

What ML system? What symbolic representation? What can we do with the symbolic representation?

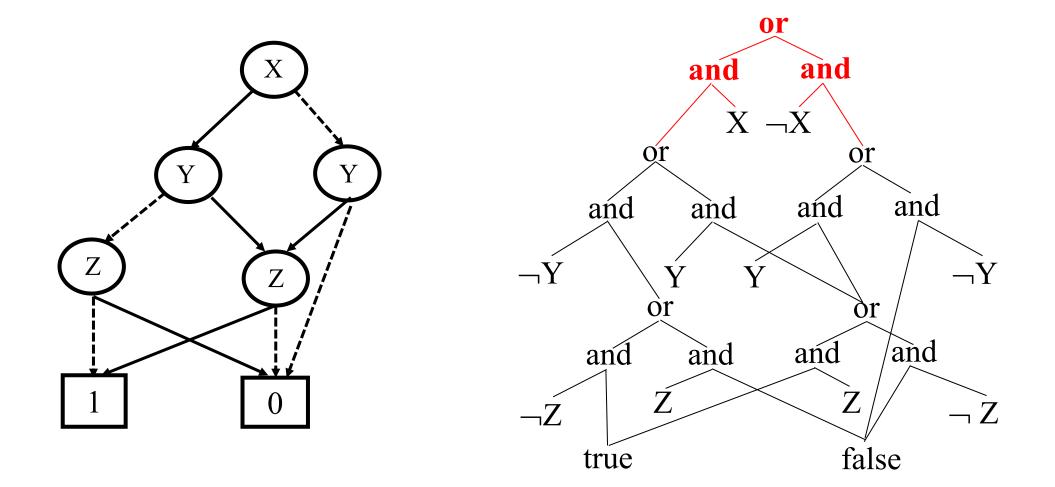


Ordered Binary Decision Diagram (OBDD)

Ordered Binary Decision Diagrams (OBDDs)

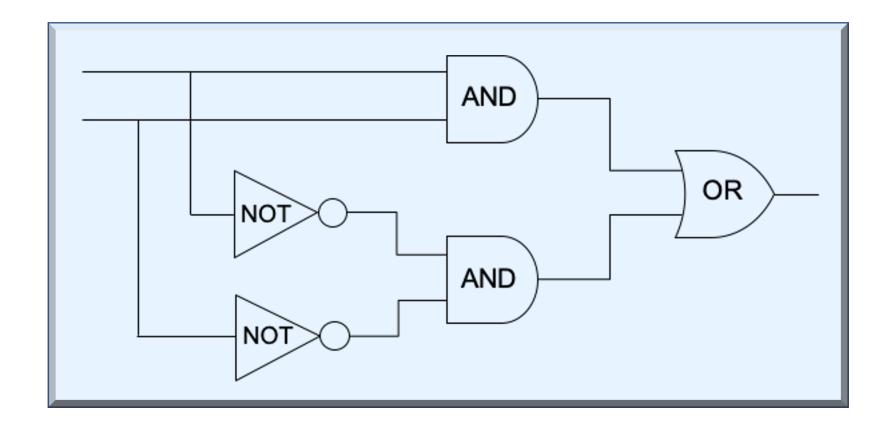


Ordered Binary Decision Diagrams (OBDDs)



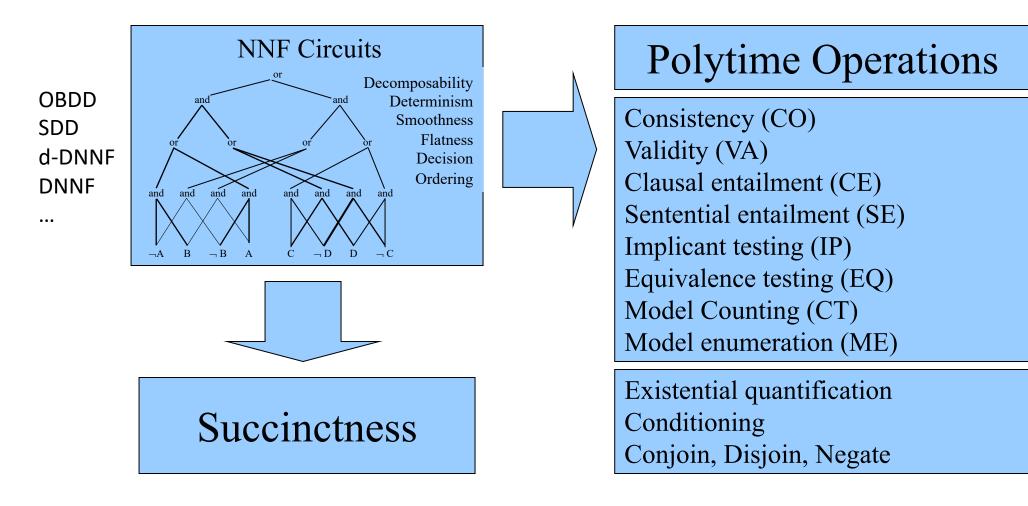
Negation Normal Form Circuits

NNF Circuits



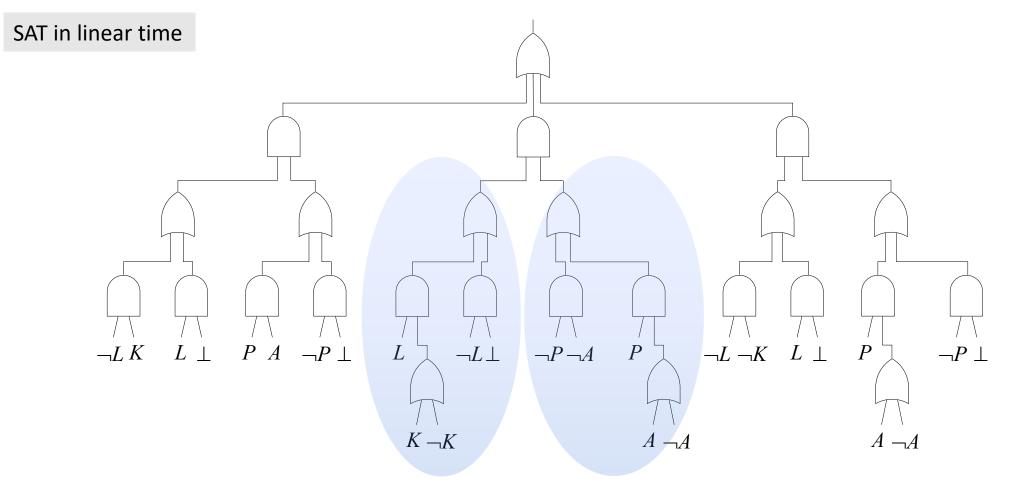
Tractable Circuits Knowledge Compilation

Darwiche & Marquis, JAIR 2002



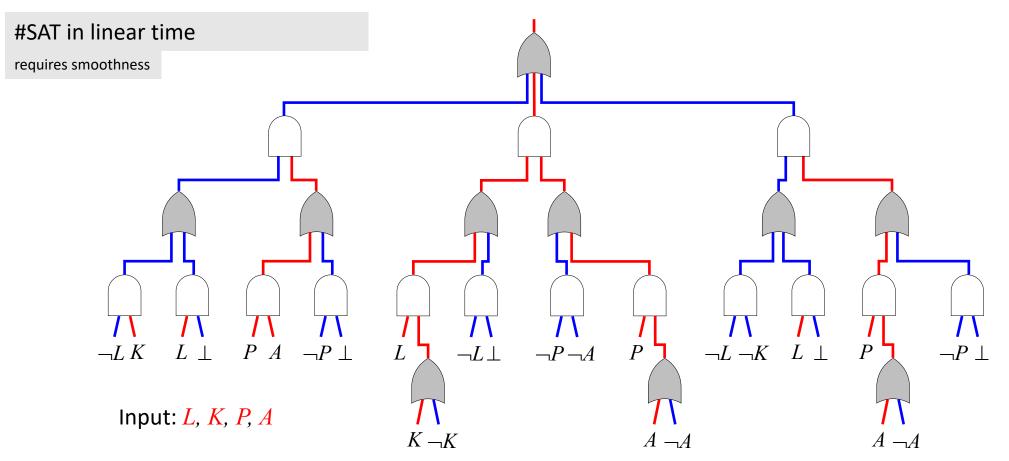
Decomposability (DNNF)

Darwiche, JACM 2001

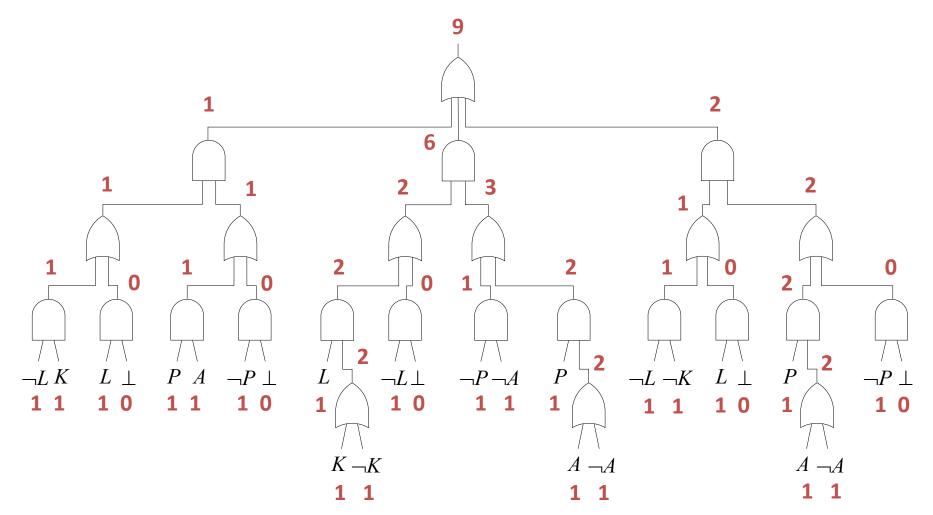


Determinism (d-DNNF)

Darwiche, JANCL 2001

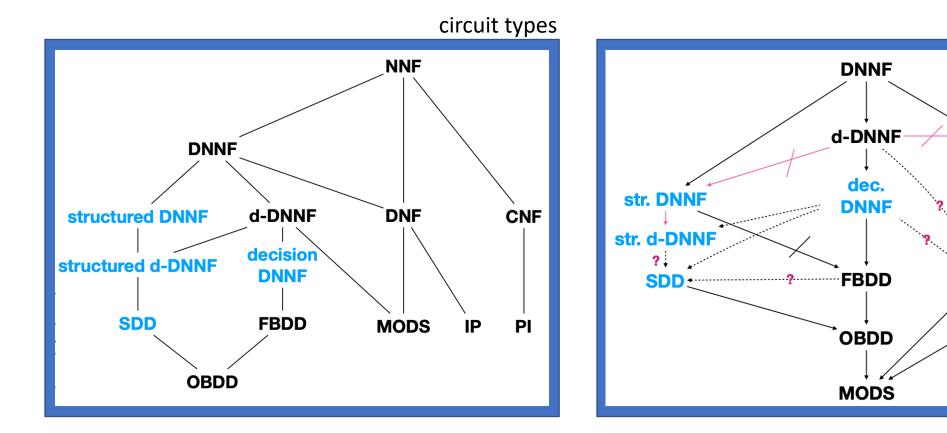


Model Counting (#SAT)



Knowledge Compilation Map

Darwiche & Marquis, JAIR 2002



KOCOON workshop 2019 Friedrich Slivovsky. An Introduction to Knowledge Compilation [87]

See PODS paper for pointers to:

knowledge compilers, model counters, weighted model counters, reduction tools and other resources

succinctness

CNF

PI

DNF

ÌΡ



Lecture 7B: Tractable Circuits & Knowledge Compilation Map

UCLA Automated Reasoning Group



Lecture 8A: DNNF Circuits (Decomposability) UCLA Automated Reasoning Group



Lecture 8B: DNNF Circuits (Minimization and Structured Decomposability) UCLA Automated Reasoning Group



Lecture 9A: d-DNNF circuits (Determinism and Smoothness)

UCLA Automated Reasoning Group

on Automated Reasoning. The course is focused on the interplay between logic, probabilistic reasoning and machine learning. The unifying theme is tractable Boolean and Arithmetic circuits 18 -1000

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CS264A: Automated Reasoning Lecture I-A

Fall 2020

Instructor: Adnan Darwiche

PLAY ALL

35 videos • 10,657 views • Last updated on Dec

Lectures by Adnan Darwiche for his UCLA course

Automated Reasoning

 \rightarrow

UCLA Automated

Reasoning Group

17, 2020

=+

X

(knowledge compilation)

Lecture 9B: Top-Down Knowledge Compilers UCLA Automated Reasoning Group



40:07

Lecture 10A: OBDD Circuits (Binary Decision Diagrams) UCLA Automated Reasoning Group



Lecture 10B: OBDD Circuits (Binary Decision Diagrams)

UCLA Automated Reasoning Group



Lecture 11A: SDD Circuits (Sentential Decision Diagrams)

UCLA Automated Reasoning Group



Lecture 11B: Bottom-Up Knowledge Compilers

UCLA Automated Reasoning Group



Lecture 12A: PSDD Circuits (Probabilistic Sentential Decision Diagrams)

Knowledge Compilation Meets X

Abhay Kumar Jha, Dan Suciu: Knowledge Compilation Meets Database Theory: Compiling Queries to Decision Diagrams. Theory Comput. Syst. 52(3): 403-440 (2013)

Simone Bova, Florent Capelli, Stefan Mengel, Friedrich Slivovsky: Knowledge Compilation Meets Communication Complexity. IJCAI 2016: 1008-1014

Shubham Sharma, Rahul Gupta, Subhajit Roy, Kuldeep S. Meel: Knowledge Compilation meets Uniform Sampling. LPAR 2018: 620-636

Compiling ML Classifiers into tractable circuits

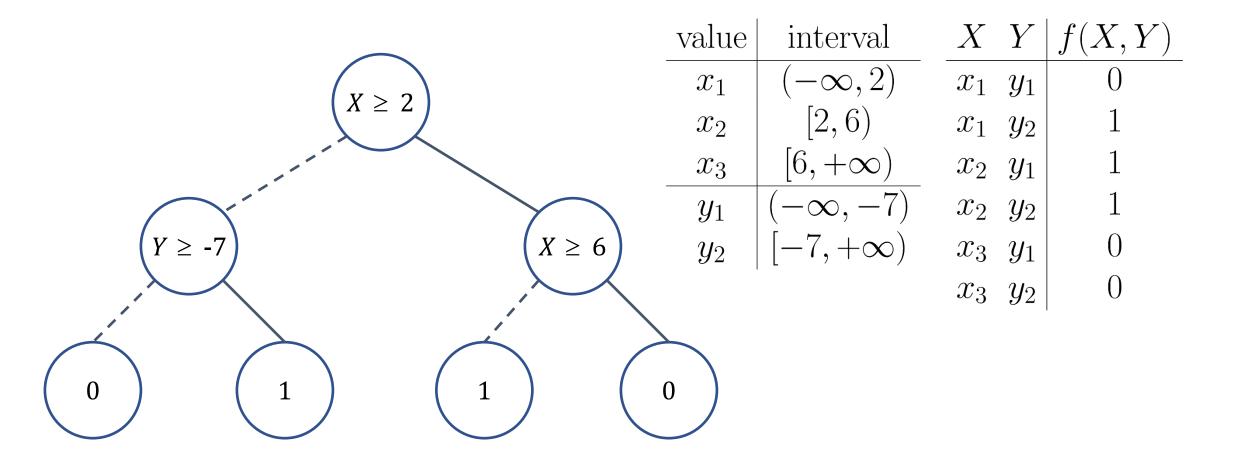
• Bayesian Networks

- Chan, Darwiche. UAI 03
- Shih, Choi, Darwiche. IJCAI 18
- Shih, Choi Darwiche. AAAI 19
- Neural Networks (restricted classes)
 - Choi, Shi, Shih, Darwiche. VNN 2019
 - Shih, Darwiche, Choi. SAT 2019
 - Shi, Shih, Darwiche, Choi. KR 2020

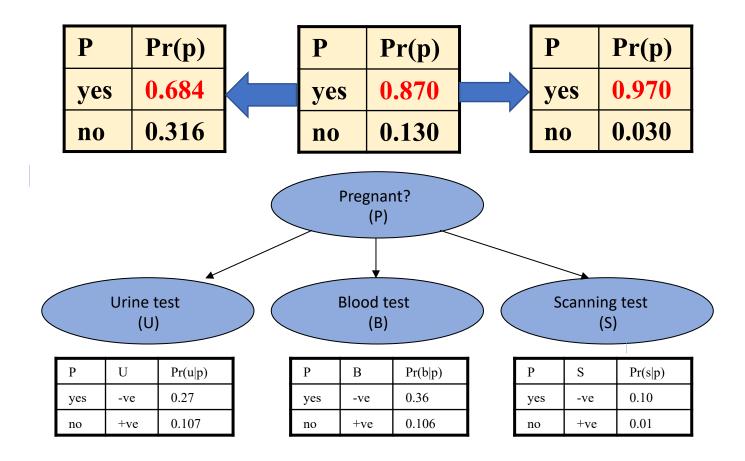
• Decision Trees & Random Forests

- Choi, Shih, Goyanka, Darwiche. FoMLAS 2020
- Audemard, Koriche, Marquis. KR 2020

Decision Tree \rightarrow Discrete Function



Numbers Don't Matter as Much



Threshold 0.9

Equivalence interval [0.684, 0.970]

Compiling ML Classifiers into tractable circuits

• Bayesian Networks

- Chan, Darwiche. UAI 03
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Decision Trees & Random Forests

- Choi, Shih, Goyanka, Darwiche. FoMLAS 2020
- Audemard, Koriche, Marquis. KR 2020



43:40

30



Explaining Decisions

PI-Explanation (Sufficient Reason) Shih, Choi & Darwiche (IJCAI 18)

minimal set of instance characteristics that can trigger the decision (other features are irrelevant)

Example Explanation

Sally tested negative for Scanning, Blood and Urine

Why did you conclude that Sally is not pregnant?

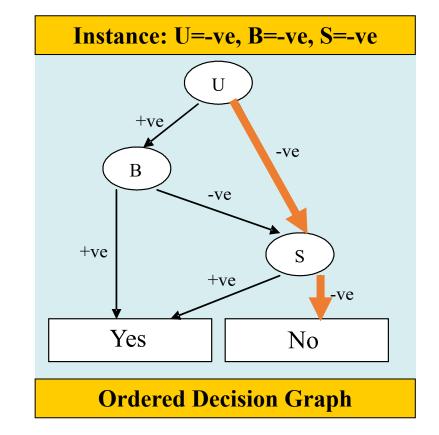
Because the Scanning test, and one of the Blood and Urine tests came out negative

sufficient reasons

- S=-ve and B=-ve
- S=-ve and U=-ve

The **complete reason** behind the decision

(S=-ve and (B=-ve or U=-ve))



Prime Implicants decades old: CS + AI

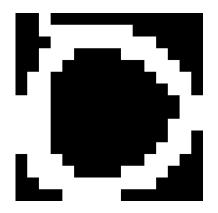
Boolean function $f = (A + \overline{C})(B + C)(A + B)$ Boolean function $\overline{f} = (A + \overline{C})(B + C)(A + B)$ Prime implicants $AB, AC, B\overline{C}$ Prime implicants $\overline{A}C, \overline{B} \ \overline{C}, \overline{A} \ \overline{B}$

Instance: $AB\overline{C}$ Decision: 1 Sufficient reasons: $AB, B\overline{C}$ Instance: \overline{ABC} Decision: 0 Sufficient reasons: \overline{AC}

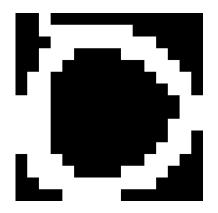
Issue: we may have an exponential number of prime implicants

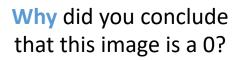
Image Classifier (0 vs 1)

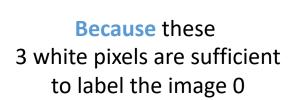
Pixels: P1,1, P16,16 Classifier f(P1,1, ..., P16,16)

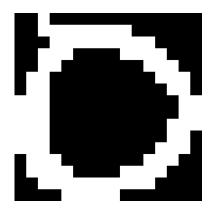


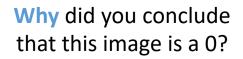
Why did you conclude that this image is a 0?

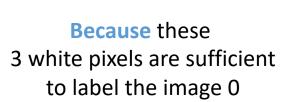


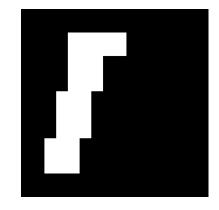








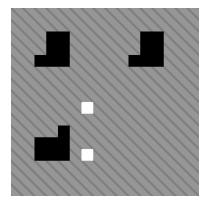




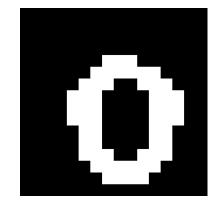
We know that this classifier will also label this image as a 0



Why did you conclude that this image is a 1?



Because these pixels are sufficient to label the image 1



We know that this classifier will also label this image as a 1

The Complete Reason Behind a Decision

Darwiche, Hirth (ECAI 2020)

disjunction of all sufficient reasons

avoids computing sufficient reasons explicitly

Reason Circuit

(for a decision)

tractable circuit representation of the complete reason

permits answering questions about sufficient reasons efficiently

Decision Bias

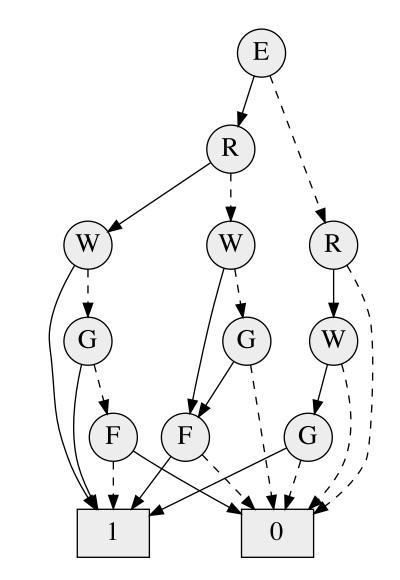
- **E**: passed the entrance exam
- **F**: first-time applicant
- G: has good grades (GPA)
- W: has work experience
- **R**: comes from a rich hometown

Decision on instance X is **biased** iff it can be different on an instance Y that disagrees with X on protected features only

Theorem: Decision is biased iff each of its sufficient reasons contains at least one protected feature

Classifier is biased iff one of its decisions is biased

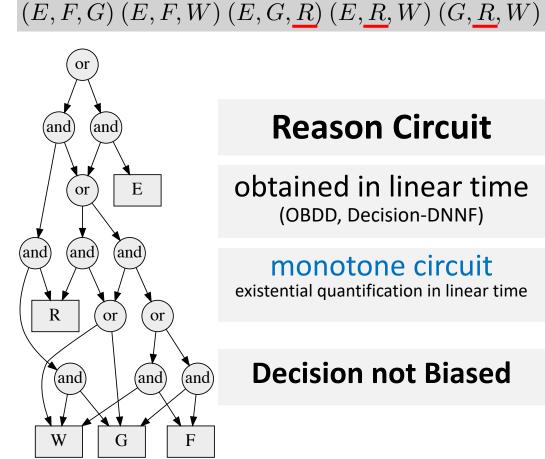
Theorem: Classifier is biased iff one of its decisions has a sufficient reason with at least one protected feature



Admissions classifier compiled into an OBDD

Complete Reason

Robin is admitted. Why?

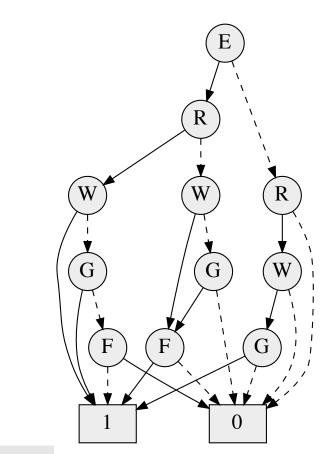


Reason Circuit

obtained in linear time (OBDD, Decision-DNNF)

monotone circuit existential quantification in linear time

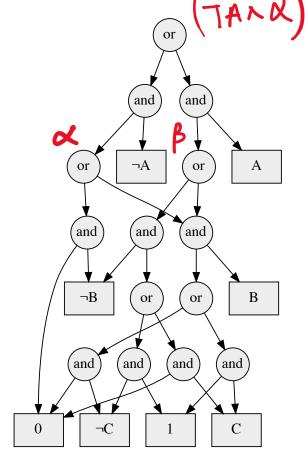
Decision not Biased



Robin

- E: passed the entrance exam
- **F**: first-time applicant
- **G**: has good grades (GPA)
- W: has work experience
- **R**: comes from a rich hometown

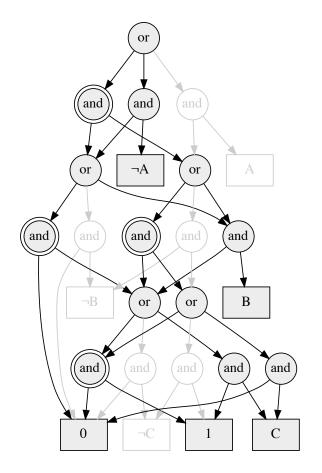
Computing the Reason Circuit



0xB (and)and and ¬Α А or or and and and and and ¬Β В or or and and and and (and 0 ¬С С







filtering

Reason Circuit

classifier

Instance: not A,B,C

What else can be done with Reason Circuits?

On The Reasons Behind Decisions. Darwiche & Hirth (ECAI 2020)

8.3 Computing Queries

Susan would still be admitted even if she did not have a high GPA because she passed the entrance exam and comes from a rich hometown. **Sufficient Reasons.** By Theorems 2 and 12, the call $PI(\Delta_{\alpha}, \alpha)$ to Algorithm 1 will return all sufficient reasons for decision $\Delta(\alpha)$, assuming Δ_{α} is a Decision-DNNF circuit. The number of sufficient reasons can be exponential, but we can actually answer many questions about them without enumerating them directly as shown below.

Necessary Property. By Proposition 4, characteristic (literal) l is necessary for decision $\Delta(\alpha)$ iff $\mathcal{R} \models l$. This is equivalent to $\mathcal{R}|\neg l$ being unsatisfiable, which can be decided in O(n) time given Theorem 10. The necessary property (all necessary characteristics) can then be computed in $O(n \cdot m)$ time.

Necessary Reason. To compute the necessary reason (if any) we compute the necessary property and check whether it satisfies the complete reason. This can be done in $O(n \cdot m)$ time.

Because Statements. To decide whether decision $\Delta(\alpha)$ was made "because τ " we check whether property τ is the complete reason for the decision (Definition 5): $\tau \models \mathcal{R}$ and $\mathcal{R} \models \tau$. We have $\tau \models \mathcal{R}$ iff $(\neg \mathcal{R})|\tau$ is unsatisfiable. Moreover, $\mathcal{R} \models \tau$ iff $\mathcal{R}|\neg l$ is unsatisfiable for every literal l in τ . All of this can be done in $O(n \cdot |\tau|)$ time.

Even if, Because Statements. To decide whether decision $\Delta(\alpha)$ would stick "even if $\overline{\rho}$ because τ " we replace property ρ with $\overline{\rho}$ in instance α to yield instance β (Definition 6). We then compute the complete reason for decision $\Delta(\beta)$ and check whether it is equivalent to τ . All of this can be done $O(n \cdot |\tau|)$ time.

Decision Bias. To decide whether decision $\Delta(\alpha)$ is biased we existentially quantify all unprotected features from circuit \mathcal{R} and then check the validity of the result (Theorem 6). All of this can be done in O(n) time given Theorems 10 and 11.

Decision and Classifier Robustness

Hamming Distance

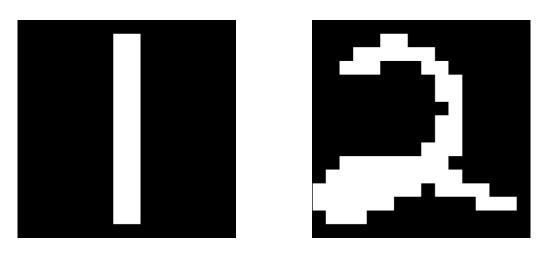
1. Instance
$$\chi=1$$
, $y=0$, $Z=1$, $W=1$
2. Instance $\chi=1$, $y=0$, $Z=0$, $W=0$
 $d(lnstance1, lnstance2) = 2$

Decision Robustness Shih, Choi, Darwiche (PGM 18)

$$\mathsf{robustness}_f(\mathbf{x}) = \min_{\mathbf{x}': f(\mathbf{x}') \neq f(\mathbf{x})} d(\mathbf{x}, \mathbf{x}')$$

- How many features do we need to flip, to flip the classifier's decision?
- Decision robustness is coNP-complete
- Linear time in an OBDD

Example: Robustness



most robust 1: robustness 3

most robust 2: robustness 13

Classifier Robustness Shi, Shih, Darwiche, Choi. KR 2020

$$\mathsf{model}\text{-}\mathsf{robustness}(f) = \frac{1}{2^n}\sum_{\mathbf{x}}\mathsf{robustness}_f(\mathbf{x})$$

the expected robustness, averaged over all possible 2ⁿ inputs

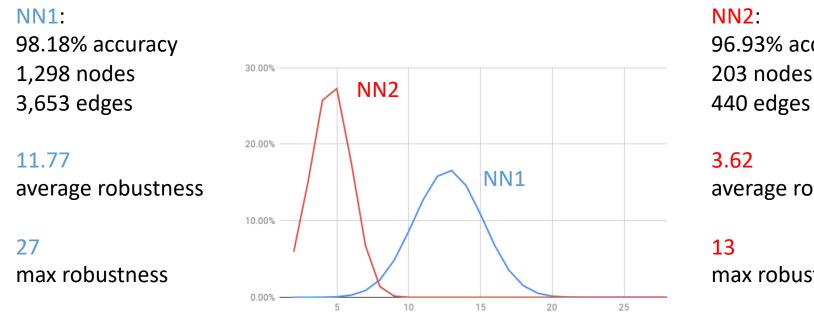
Classifier Robustness

$$\mathsf{model-robustness}(f) = \frac{1}{2^n} \sum_{\mathbf{x}} \mathsf{robustness}_f(\mathbf{x})$$

$$model-robustness(f_{parity}) = 1$$

$$\mathsf{model-robustness}(f_{\mathrm{or}}) = \frac{n}{2} + \frac{1}{2^n}$$

Classifier Robustness Shi, Shih, Darwiche, Choi. KR 2020



96.93% accuracy 203 nodes

average robustness

max robustness

http://reasoning.cs.ucla.edu/xai

Verification: Monotone Classifiers Shih, Choi, Darwiche (PGM 18)

Positive decision remains positive if we flip some features from – to +.

If instance (+,-,-,+) gives positive decision, these also give positive decision:

- (+,+,-,+)
 (+,-,+,+)
- (+,+,+,+)

Educational Testing:

Susan's correct answers include Jack's correct answers

Susan should pass if Jack passed

Credit Application:

Susan and Jack have the same characteristics, except that Susan has a higher income Susan should be approved if Jack is approved

Verification: Monotone Classifiers Shih, Choi, Darwiche (PGM 18)

- Quadratic complexity on OBDDs
- Educational assessment classifier not monotone (threshold ½)
- Cancer classifier not monotone (threshold .02 based on BI-RADS assessment scale)
- Two patients, same mammography report except for personal history.
 - One with personal history \rightarrow Benign
 - One with no personal history \rightarrow Malignant

Reasoning about the Behavior of ML Systems

new role for symbolic AI & CS methods

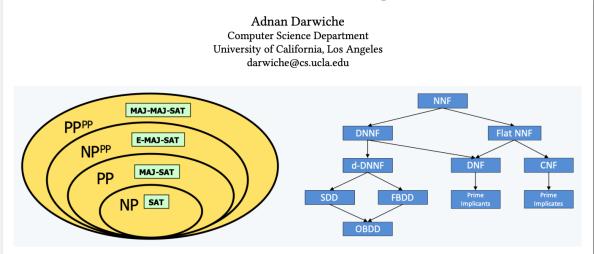
Reason About What Was Learned

Systems 1 / 2 (thinking fast and slow), reflection, meta-reasoning

compile-then-reason paradigm

(VNN community: other techniques including SAT)

Three Modern Roles for Logic in Al





ABSTRACT

We consider three modern roles for logic in artificial intelligence, which are based on the theory of tractable Boolean circuits: (1) logic as a basis for computation, (2) logic for learning from a combination of data and knowledge, and (3) logic for reasoning about the behavior of machine learning systems.

CCS CONCEPTS

• Computing methodologies → Learning in probabilistic graphical models; Logical and relational learning; • Theory of computation → Automated reasoning; Complexity classes; Problems, reductions and completeness.

KEYWORDS

tractable circuits, knowledge compilation, explainable AI

ACM Reference Format:

Adnan Darwiche. 2020. Three Modern Roles for Logic in AI. In Proceedings of the 39th ACM SIGMOD-SIGACT-SIGAI Symposium on Principles of Database Systems (PODS'20), June 14–19, 2020, Portland, OR, USA. ACM, New York, NY, USA, 15 pages. https://doi.org/10.1145/3375395.3389131

1 INTRODUCTION

Logic has played a fundamental role in artificial intelligence since the field was incepted [52]. This role has been mostly in the area of knowledge representation and reasoning, where logic is used to

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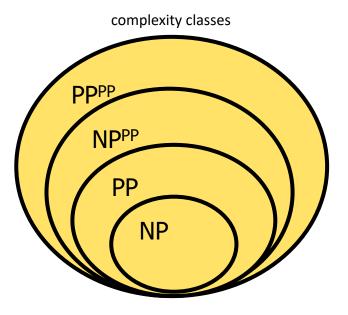
© 2020 Copyright held by the owner/author(s). ACM ISBN 978-1-4503-7108-7/20/06. https://doi.org/10.1145/3375395.3389131 represent categorical knowledge and then draw conclusions based on deduction and other more advanced forms of reasoning. Starting with [59], logic also formed the basis for drawing conclusions from a mixture of categorial and probabilistic knowledge.

In this paper, we review three modern roles for propositional logic in artificial intelligence, which are based on the theory of tractable Boolean circuits. This theory, which matured considerably during the last two decades, is based on Boolean circuits in Negation Normal Form (NNF) form. NNF circuits are not tractable, but they become tractable once we impose certain properties on them [34]. Over the last two decades, this class of circuits has been studied systematically across three dimensions. The first dimension concerns a synthesis of NNF circuits that have varying degrees of tractability (the polytime queries they support). The second dimension concerns the relative succinctness of different classes of tractable NNF circuits (the optimal size circuits can attain). The third dimension concerns the development of algorithms for compiling Boolean formula into tractable NNF circuits.

The first modern role for logic we consider is in using tractable circuits as a basis for computation, where we show how problems in the complexity classes NP, PP, NP^{PP} and PP^{PP} can be solved by compiling Boolean formula into corresponding tractable circuits. These are rich complexity classes, which include some commonly utilized problems from probabilistic reasoning and machine learning. We discuss this first role in two steps. In Section 2, we discuss the prototypical problems that are complete for these complexity classes, which are all problems on Boolean formula. We also discuss problems from probabilistic reasoning which are complete for these classes and their reduction to prototypical problems. In Section 3, we introduce the theory of tractable circuits with exposure to circuit types that can be used to efficiently solve problems in these complexity classes (if compiled successfully).

Logic For Computation

reducing NP & 'Beyond NP' problems to logical reasoning



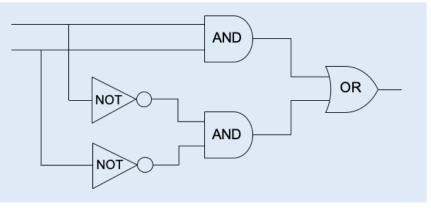
prototypical problems (on Boolean formula)

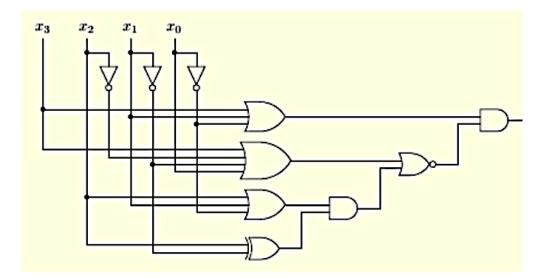
((A or B) and (not C)) or (not B and D)

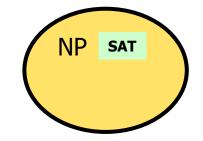
complete problems (probabilistic reasoning & ML)

			S		С	$\theta_{c s}$	С	T_1	$\theta_{t_1 c}$
<u>_</u>	S	θ_s	ma	le	yes	.05	yes	; +v	
	male	.55	ma	le	no	.95	yes	s −v	e .20
	female	.45	fen	nale	yes	.01	no	+v	e .20
			fen	nale	no	.99	no	-v	e .80
	S	С	T_2	$\theta_{t_2 c}$,5	T_1	T_2	A	$\theta_{a t_1,t_2}$
	male	yes	+ve	.80		+ve	+ve	yes	1
	male	yes	-ve	.20		+ve	+ve	no	0
	male	no	+ve	.20		+ve	-ve	yes	0
	male	no	-ve	.80		+ve	-ve	no	1
	female	yes	+ve	.95		-ve	+ve	yes	0
	female	yes	-ve	.05		-ve	+ve	no	1
	female	no	+ve	.05		-ve	-ve	yes	1
	female	no	-ve	.95		-ve	-ve	no	0

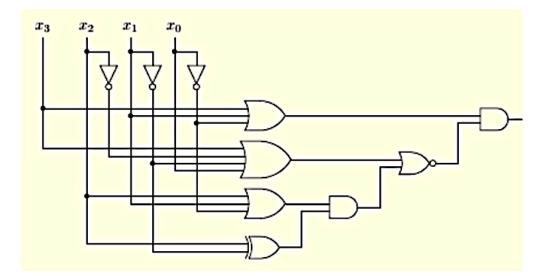
tractable Boolean circuits (essence of computation)



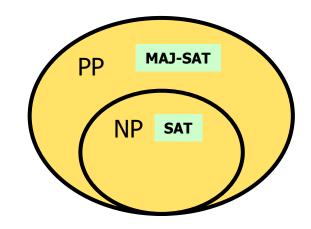


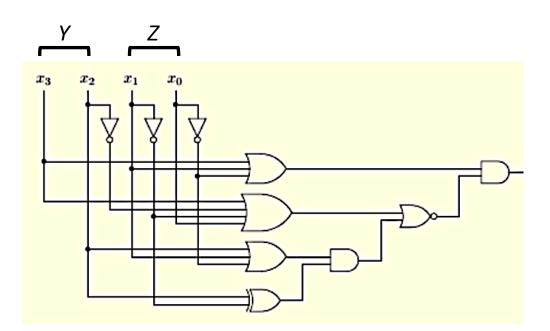


Circuit input that generates 1-output?

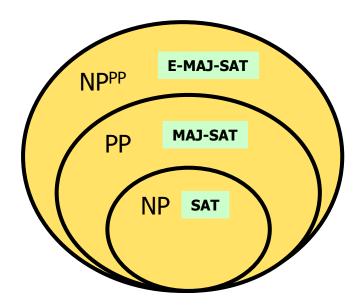


Majority of circuit inputs generate 1-output?

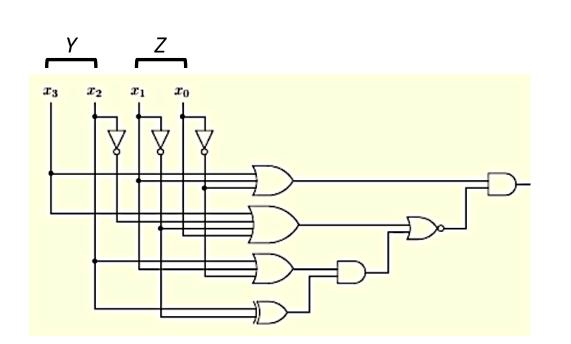


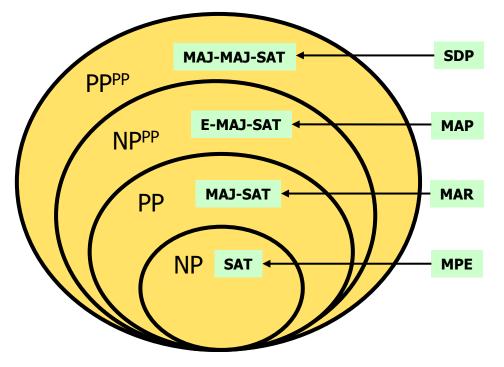


Y-input under which majority of Z-inputs generate 1-output?



probabilistic reasoning

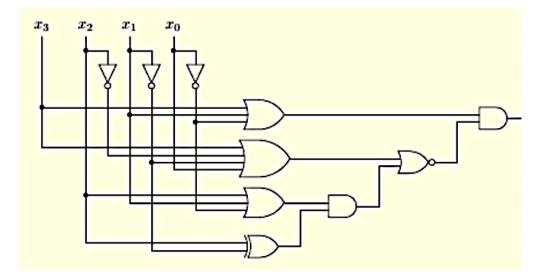




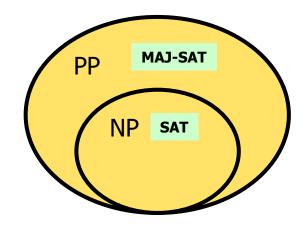
Majority of Y-inputs under which majority of Z-inputs generate 1-output?

weights

 $w(x_0=0), w(x_0=1) \dots w(x_3=0), w(x_3=1)$

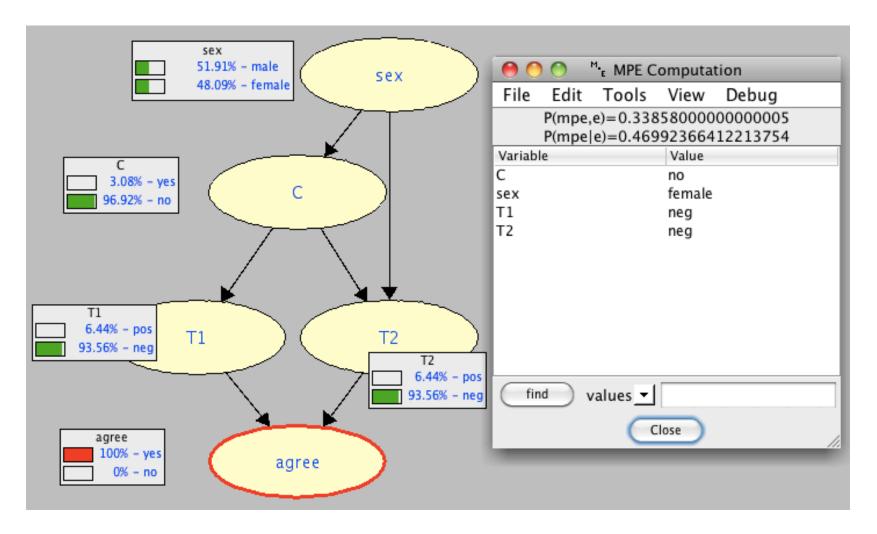


Majority of circuit inputs generate 1-output? Count of circuit inputs that generate 1-output? (#SAT) Weighted count of circuit inputs that generate 1-output? (WMC)



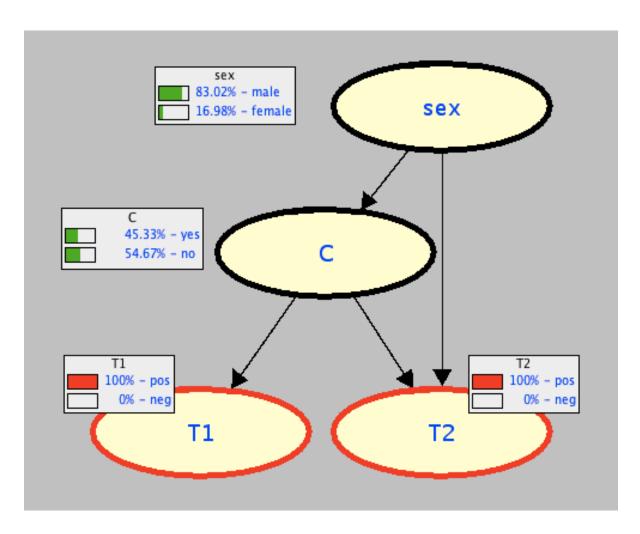
NP-complete query

Most Probable Explanation (MPE)



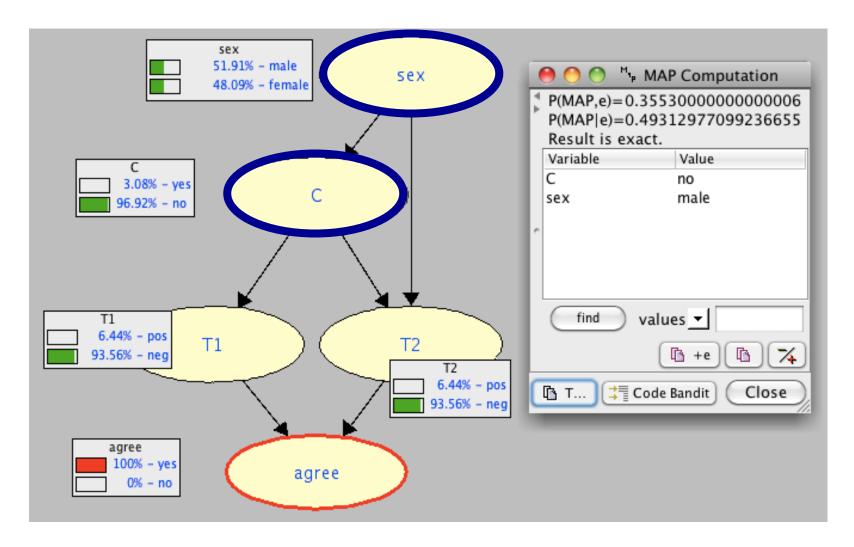
PP-complete query

Marginal Probabilities (MAR)



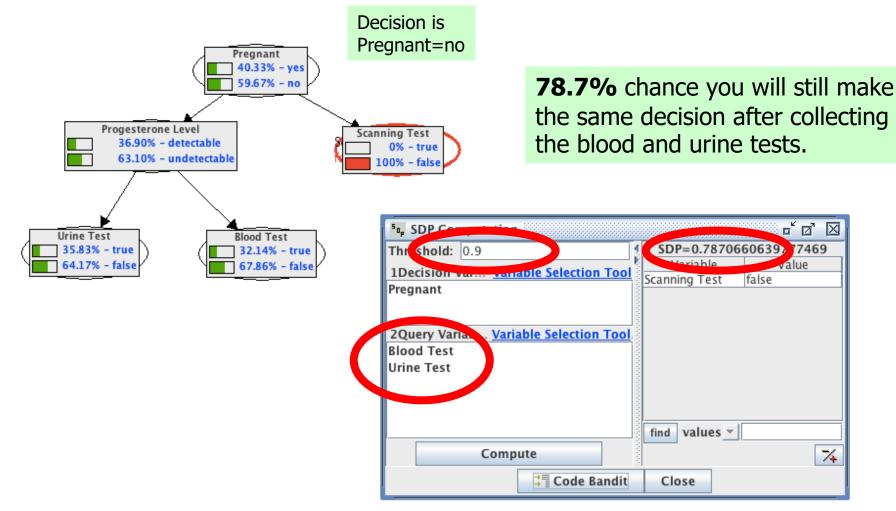
NP^{PP}-complete query

Maximum a Posterior Hypothesis (MAP)



PP^{PP}-complete query

Same-Decision Probability (SDP) Darwiche & Choi, PGM 2010



On Probabilistic Reasoning by Weighted Model Counting. Chavira and Darwiche, AIJ 2008

$$w(A) = w(\neg A) = w(B) = w(\neg B) = w(C) = w(\neg C) = 1$$

$$w(\neg P_{\alpha|\beta}) = 1$$

$$w(P_{\alpha|\beta}) = \theta_{\alpha|\beta}$$

$$A \land B \Leftrightarrow P_{A|B}$$

$$T T F \theta_{A}\theta_{B|A}\theta_{-C|A}$$

$$T T F \theta_{A}\theta_{B|A}\theta_{-C|A}$$

$$T T F \theta_{A}\theta_{B|A}\theta_{-C|A}$$

$$W(A) = \theta_{A}\theta_{B|A}\theta_{-C|A}$$

$$F F \theta_{-A}\theta_{-B|\neg A}, \neg P_{-B|\neg A}, \neg P_{-B|\neg A}, \neg P_{-C|\neg A}, \neg P_{-C|\neg A}$$

$$w(m) = \theta_{A}\theta_{B|A}\theta_{-C|A}$$

$$Pr(\delta) = wmc(\Delta \land \delta)$$

Darwiche, KR 2002

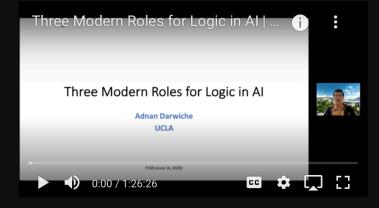
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